



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
Shunpei YAMAZAKI ) Examiner: Remmon R. Forde  
Serial No. 10/079,512 ) Group Art Unit: 2826  
Filed: February 22, 2002 )  
For: SEMICONDUCTOR DEVICE )

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

Please note that Serial No. 09/443,705 (which is noted on the attached PTO-1449) is considered to be related to the above noted application. In addition, Applicant has provided a copy of the Official Action of March 12, 2004 as well as the reference cited by the Examiner in this action (JP8-264784) for Serial No. 09/443,705.

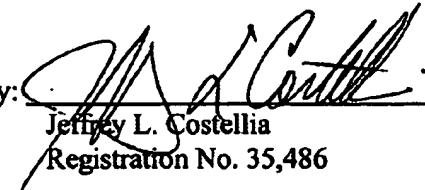
In addition to the above, applicant wishes to note a misspelling of the inventor's name of Japanese Patent No. 06-148685 to NAKAZAONO et al., cited on page 2 of the PTO-1499 filed on October 6, 2003. The correct spelling inventor's name is "NAKAZONO et al.". In view of this error, Applicant hereby corrects the misspelling on the attached PTO-1449. Therefore, it is respectfully requested that the Examiner mark through JP-06-148685 TO NAKAZAONO et al. appearing on the October 6, 2003 PTO-1449 so the correct spelling will be printed on the face of the patent resulting from the instant application.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initial a copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By:

  
Jeffrey L. Costellia  
Registration No. 35,486

NIXON PEABODY LLP  
Suite 900  
401 9<sup>th</sup> Street, N.W.  
Washington, DC 20004-2128

Telephone: (202) 585-8000



Substitute for form 1449A/PTO				<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	10/079,512
Sheet	1	of	1	Filing Date	February 22, 2002
				First Named Inventor	Shunpei YAMAZAKI
				Art Unit	2826
				Examiner Name	Remmon R. Forde
				Attorney Docket Number	740756-2441

U.S. PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	U.S. Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Column, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
		US-			

FOREIGN PATENT DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Column, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup> Number <sup>4</sup> (if known)			
		JP-06-148685	05/27/1994	NAKAZONO et al.	Full
		JP-08-264784	10/11/1996	KUROKAWA	Partial

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
		U.S. Patent Application Serial No. 09/433,705; Filed: November 4, 1999; Shunpei Yamazaki; "Method of Fabricating a Semiconductor Device" (Specification; Claims, Abstract, Drawings, Official Filing Receipt and Official Action dated 3/12/04 with Partial English Translation of Japanese Patent No. 8-264784); Status: Pending			

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.